FORM PTO-1449 (SUBSTITUTE) U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT (37 CFR 1.98(b))	Attorney Docket No.: P2001,0241 Appl. No.: 10/678 385 Applicant: JUDITH MAGET ET AL. Filing Date: October 3, 2003 Group Art Unit: 2813

EXAMINER INITIALS		PATENT NO.	DATE	PATENTEE	CLASS	SUB CLASS	FILING DATE
	A	6,172,378 B1	1/9/01	Hull et al.	—	—	
	B	5,965,912	10/12/99	Stolfa et al.	—	—	
	C	6,351,020 B1	2/26/02	Tarabbia et al.	—	—	
	D	6,034,388	3/7/00	Brown et al.	—	—	
	E						
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FOREIGN PATENT DOCUMENT

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB CLASS	TRANSL. YES	TRANSL. NO
	J	03147376 A	6/24/91	Japan				
	K	1 024 538 A1	8/2/00	Europe				
	L							
	M							
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		Svelto, F. et al.: "A Three Terminal Varactor for RF IC's in Standard CMOS Technology", IEEE Transactions on Electron Devices, Vol. 47, No. 4, April 2000, pp. 893-895
		Castello, R. et al.: "A +-30% Tuning Range Varactor Compatible with future Scaled Technologies", IEEE, Symposium on VLSI Circuits Digest of Technical Papers, 1998, pp. 34-35

EXAMINER	DATE CONSIDERED
	4/22/05

FORM PTO-1449 (SUBSTITUTE) U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT (37 CFR 1.98(b))	Attorney Docket No.: P2001,0241 Appl. No.: 10/678 385 Applicant: JUDITH MAGET ET AL. Filing Date: October 3, 2003 Group Art Unit: 2813

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JMG		Wong, W. M. Y. et al.: "A Wide Tuning Range Gated Varactor", IEEE Journal of Solid-State Circuits, Vol. 35, No. 5, May 2000, pp. 773-779
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EXAMINER	DATE CONSIDERED
	4/26/05

FORM PTO-1449 (SUBSTITUTE) U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT (37 CFR 1.98(b))	Attorney Docket No.: P2001,0241 Appl. No.: 10/678 385 Applicant: JUDITH MAGET ET AL. Filing Date: October 3, 2003 Group Art Unit: 2813

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FOREIGN PATENT DOCUMENT

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		Andreani, P. et al.: "On the Use of MOS Varactors in RF VCO's", IEEE Journal of Solid-State Circuits, Brief Papers, Vol. 35, No. 6, June 2000, pp. 905-910
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	4/22/05